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AND TRADEMARK OFFICE**

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filed September 24, 2003)

Applicant(s): Katsura HIRAI

Filed : CONCOMITANTLY HEREWITH

For : ELECTRICAL CIRCUIT, THIN
FILM TRANSISTOR, PRODUCTION
METHOD OF ELECTRICAL CIRCUIT,
AND PRODUCTION METHOD OF
THIN FILM TRANSISTOR

Art Unit :

Examiner :

Docket No. : 05170/HG

Confirm. No.:

Customer No.: 01933

**INFORMATION DISCLOSURE STATEMENT
FILED CONCOMITANT WITH NATIONAL
PHASE PCT APPLICATION**

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Submitted herewith are the following:

- (1) Copy of International Search Report issued in the
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- (2) Copies of the cited publications are not enclosed
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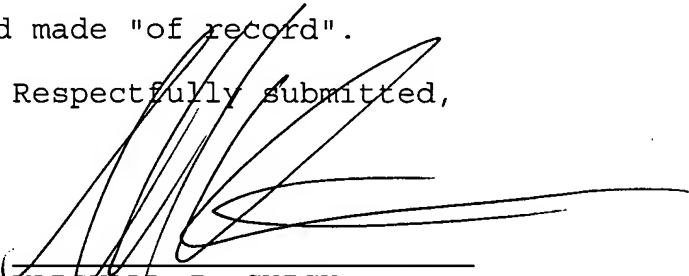
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Each of the cited publications is considered relevant or material to the patentability of the present invention in view of the citation thereof in said communication and for the reasons stated in said communication.

It is therefore respectfully requested that the publications be considered and made "of record".

Frishauf, Holtz, Goodman
& Chick, P.C.
767 Third Ave., 25th Floor
New York, NY 10017-2023
Tel. No. (212) 319-4900
Fax No.: (212) 319-5101
MJC/ld

Respectfully submitted,



MARSHALL J. CHICK
Reg. No. 26,853

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				Filing Date		Herewith	
				First Named Inventor		Katsura HIRAI	
				Group Art Unit			
				Examiner Name			
Sheet	1	of	1	Attorney Docket Number		05170/HG	

U.S. PATENT DOCUMENTS

Exam. Inits*	Cite No ¹	Document Number	Kind Code ²	Name of Patentee or Applicant	Publication Date MM-DD-YYYY	Relevant Portion

FOREIGN PATENT DOCUMENTS

Exam Inits*	Cite No ¹	Offc ³	Document Number ⁴	Kind Code ⁵	Name of Patentee or Applicant	Publication Date MM-DD-YYYY	Relevant Portion	T ⁶
		JP	2000-193922	A	Seiko Epson Corp.	07-14-2000		
		JP	2002-164635	A	Seiko Epson Corp.	06-07-2002		
		EP	930641	A2	Seiko Epson Corp.	07-21-1999		
		JP	11-274681	A	Seiko Epson Corp.	10-08-1999		
		JP	2002-26014	A	Seiko Epson Corp.	01-25-2002		
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		JP	2003-318193	A	Seiko Epson Corp.	11-07-2003		
		JP	2003-318401	A	Seiko Epson Corp.	11-07-2003		

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